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Electronic Version v1.1

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Title of	
Invention	

METHODS AND STRUCTURES FOR PROTECTING ONE AREA WHILE PROCESSING ANOTHER AREA ON A CHIP

Application Number:

Date:

First Named Applicant:

Deok-kee Kim

Confirmation Number:

Attorney Docket Number:

FIS920040058

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Submitted By:	Elec. Sign.	Sign. Capacity
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Registered Number: 30,238		

Documents being submitted:	Files
us-assignment	FIS920040058US1-usassn.xml
-	us-assignment.xsl
	us-assignment.dtd
	FIS920040058US1assign1.tif
	FIS920040058US1assign2.tif
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us-request	FIS920040058US1-usrequ.xml
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	us-request.xsl
us-fee-sheet	FIS920040058US1-usfees.xml
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application-body	FIS920040058-trans.xml
	us-application-body.xsl
	application-body.dtd
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	mathml2.dtd
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us-declaration	FIS920040058US1dec5.tif	

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